



SHEET 1 OF 1

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				APPLICANT Shailender Chaudhry, et al.			
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U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS							
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						Yes	No
GZ	0 357 188	07.03.90	EPO				
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
GZ	IBM Technical Disclosure Bulletin entitled "Deferred decoding", Vol. 27, No. 10B, March 1985 IBM Corp., Pages 6257-6258, XP-000885126.						
GZ	Publication entitled "Runahead Execution: An Alternative to Very Large Instruction Windows for Out-of-order Processors", by Onur Mutlu et al., Proceedings of The Ninth International Symposium on High-Performance computer Architecture, IEEE, 2002						
GZ	Publication entitled "Beating in-order Stalls with "Flea-Flicker" two-pass Pipelining", by Ronald D. Barnes et al., Proceedings of the 36 th International Symposium on Microarchitecture, IEEE, 2003.						
EXAMINER /George Zalepa/				DATE CONSIDERED 06/23/2006			

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